

L Number	Hits	Search Text	DB	Time stamp
1	18069	((second or two or multiple or plural\$3) near3 (SRAM or bank or (static adj random adj access adj memory)))	USPAT	2003/10/22 11:50
2	11	((second or two or multiple or plural\$3) near3 (SRAM or bank or (static adj random adj access adj memory))) with (ECC or (error adj check\$3) or parity) with (multiplex\$3 or select\$3 or switch\$3)	USPAT	2003/10/22 11:23
3	31559	((second or two or multiple or plural\$3) near3 (SRAM or bank or (static adj random adj access adj memory))) with (ECC or (error adj check\$3) or parity)	USPAT	2003/10/22 11:23
4	0	((second or two or multiple or plural\$3) near3 (SRAM or bank or (static adj random adj access adj memory))) with ((ECC or (error adj check\$3) or parity)) with multiplex\$3	USPAT	2003/10/22 11:24
5	23	((second or two or multiple or plural\$3) near3 (SRAM or bank or (static adj random adj access adj memory))) same ((ECC or (error adj check\$3) or parity)) same multiplex\$3	USPAT	2003/10/22 11:27
6	3881	((second or two or multiple or plural\$3) near3 (SRAM or bank or (static adj random adj access adj memory))) with (multiplex\$3 or select\$3 or switch\$3)	USPAT	2003/10/22 11:27
7	25	((second or two or multiple or plural\$3) near3 (SRAM or bank or (static adj random adj access adj memory))) with (multiplex\$3 or select\$3 or switch\$3) same ((ECC or (error adj check\$3) or parity))	USPAT	2003/10/22 11:28
8	15179	((second or two) near4 (SRAM or bank or (static adj random adj access adj memory)))	USPAT	2003/10/22 11:56
9	68	((second or two) near4 (SRAM or bank or (static adj random adj access adj memory))) with ((ECC or (error adj check\$3) or parity))	USPAT	2003/10/22 11:51
10	21442	((second or two) near4 (SRAM or bank or (static adj random adj access adj memory) or cache))	USPAT	2003/10/22 11:58
11	708	((second or two) near4 (SRAM or bank or (static adj random adj access adj memory) or cache)) with multiplex\$3	USPAT	2003/10/22 11:56
12	1	((second or two) near4 (SRAM or bank or (static adj random adj access adj memory) or cache)) with multiplex\$3) with ((ECC or (error adj check\$3) or parity))	USPAT	2003/10/22 11:57
13	13	((second or two) near4 (SRAM or bank or (static adj random adj access adj memory) or cache)) with multiplex\$3) same ((ECC or (error adj check\$3) or parity))	USPAT	2003/10/22 11:57
14	44583	((second or two) near4 (SRAM or bank or (static adj random adj access adj memory) or cache) or ("L2" near1 cache))	USPAT	2003/10/22 11:59
15	8287	((second or two) near3 (SRAM or (static adj random adj access adj memory) or cache or ("L2" near1 cache))	USPAT	2003/10/22 12:00
16	102	((second or two) near3 (SRAM or (static adj random adj access adj memory) or cache or ("L2" near1 cache))) near5 multiplex\$3	USPAT	2003/10/22 12:00
17	0	((second or two) near3 (SRAM or (static adj random adj access adj memory) or cache or ("L2" near1 cache))) near5 multiplex\$3) same ((ECC or (error adj check\$3) or parity))	USPAT	2003/10/22 12:00

18	30	((second or two) near3 (SRAM or (static adj random adj access adj memory) or cache or ("L2" near1 cache))) near5 multiplex\$3) and ((ECC or (error adj check\$3) or parity))	USPAT	2003/10/22 12:03
19	226	((second or two) near3 (SRAM or (static adj random adj access adj memory) or cache or ("L2" near1 cache))) same ((ECC or (error adj check\$3) or parity))	USPAT	2003/10/22 12:03
20	21	((second or two) near3 (SRAM or (static adj random adj access adj memory) or cache or ("L2" near1 cache))) same ((ECC or (error adj check\$3) or parity)) same multiplex\$3	USPAT	2003/10/22 12:04
21	36	((second or two) near3 (SRAM or (static adj random adj access adj memory) or cache or ("L2" near1 cache))) with (ECC or (error adj check\$3))	USPAT	2003/10/22 12:09
22	9653	((primary or secondary) near3 (cache or memory or SRAM or bank))	USPAT	2003/10/22 12:10
23	2	((primary or secondary) near3 (cache or memory or SRAM or bank)) with multiplex\$3 with ((ECC or (error adj check\$3) or parity))	USPAT	2003/10/22 12:23
24	114	((primary or secondary) near3 (cache or memory or SRAM or bank)) with ((ECC or (error adj check\$3) or parity))	USPAT	2003/10/22 12:23
25	0	((primary or secondary) near3 (cache or memory or SRAM or bank)) with ((ECC or (error adj check\$3) or parity))) same (single near1 chip)	USPAT	2003/10/22 12:23
26	13	((primary or secondary) near3 (cache or memory or SRAM or bank)) with ((ECC or (error adj check\$3) or parity))) and (single near1 chip)	USPAT	2003/10/22 12:25
27	25	((primary or secondary) near3 (cache or memory or SRAM or bank)) with ((ECC or (error adj check\$3) or parity))) same (multiplex\$3 or select\$3 or switch\$3)	USPAT	2003/10/22 12:25